



Course Weekly Outline

Course Name: Digital Signal Processing II

Course Instructor	Dr. Omar Munthir Al Okashi				
E-mail	Omar.alokashi@uoanabr.edu.iq				
Title	Ass. Prof				
Course Coordinator					
Course Objective	The purpose of the course is to introduce principles of computer organization and the basic architectural concepts. It begins with basic organization, design, of a simple digital computer and introduces simple register transfer language to specify various computer operations.				
Course Description	This course aims to provide a strong foundation for students to understand the modern eras of computer architecture. The course is structured around different main subject of computer architecture. Those subjects include different parts of computer such as memory, CPU and input output devices.				
Textbook	The essential of computer architecture and organization, 5 th edition, Linda Null				
References	The essential of computer architecture and organization, 5 th edition, Linda Null				
Course Assessments	Term Tests	Laboratory	Quizzes	Project	Final Exam
	35	-	5	-	60
General Notes	-				



Week	Date	Topics Covered	Lab. Experiment Assignments	Notes
1	21-02	Introduction to computer components and historical review		
2	28-02	Data representation in computer system		
3	07-03	Error detection and correction		
4	14-03	Boolean algebra and digital logic		
5	21-03	Exam		
6	28-03	MARIE: an introduction to simple computer		
7	04-04	Instruction Set Architecture		
8	11-04	Memory (1)		
9	18-04	Memory (2)		
10	25-04	Exam		
11	02-05	Input/output storage system		
12	09-05	System Software		
13	16-05	Performance Measurement and Analysis		
14	23-05	Embedded System		
15	30-05	Exam		

Course Weekly Outline

Instructor Signature:

Dean Signature: